MUR1100E is a Preferred Device

SWITCHMODE [™] Power Rectifiers Ultrafast "E" Series with High Reverse Energy Capability

These state-of-the-art devices are designed for use in switching power supplies, inverters and as free wheeling diodes.

Features

- 10 mjoules Avalanche Energy Guaranteed
- Excellent Protection Against Voltage Transients in Switching Inductive Load Circuits
- Ultrafast 75 Nanosecond Recovery Time
- 175°C Operating Junction Temperature
- Low Forward Voltage
- Low Leakage Current
- High Temperature Glass Passivated Junction
- Reverse Voltage to 1000 V
- These are Pb–Free Devices*

Mechanical Characteristics:

- Case: Epoxy, Molded
- Weight: 0.4 Gram (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Shipped in Plastic Bags; 1,000 per Bag
- Available Tape and Reel; 5,000 per Reel, by Adding a "RL" Suffix to the Part Number
- Polarity: Cathode Indicated by Polarity Band

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage MUR180E MUR1100E	V _{RRM} V _{RWM} V _R	800 1000	V
Average Rectified Forward Current (Note 1) (Square Wave Mounting Method #3 Per Note 3)	I _{F(AV)}	1.0 @ T _A = 95°C	A
Non-Repetitive Peak Surge Current (Surge applied at rated load conditions, halfwave, single phase, 60 Hz)	I _{FSM}	35	A
Operating Junction Temperature and Storage Temperature Range	T _J , T _{stg}	−65 to +175	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability. 1. Pulse Test: Pulse Width = $300 \ \mu$ s, Duty Cycle $\leq 2.0\%$.

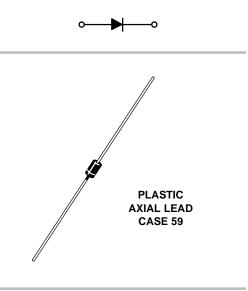
*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



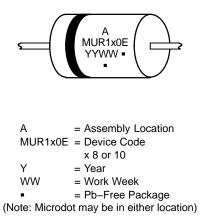
ON Semiconductor®

http://onsemi.com

ULTRAFAST RECTIFIERS 1.0 AMPERES, 800–1000 VOLTS



MARKING DIAGRAM



ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

Preferred devices are recommended choices for future use and best overall value.

THERMAL CHARACTERISTICS

Charateristics	Symbol	Value	Unit
Maximum Thermal Resistance, Junction-to-Ambient	R _{0JA}	See Note 3	°C/W
ELECTRICAL CHARACTERISTICS	· · · ·		·
Maximum Instantaneous Forward Voltage (Note 2) ($i_F = 1.0 \text{ Amp}, T_J = 150^{\circ}\text{C}$) ($i_F = 1.0 \text{ Amp}, T_J = 25^{\circ}\text{C}$)	VF	1.50 1.75	V
Maximum Instantaneous Reverse Current (Note 2) (Rated dc Voltage, $T_J = 100^{\circ}C$) (Rated dc Voltage, $T_J = 25^{\circ}C$)	i _R	600 10	μΑ
	t _{rr}	100 75	ns
Maximum Forward Recovery Time $(I_F = 1.0 \text{ Amp}, \text{ di/dt} = 100 \text{ Amp}/\mu s, \text{ Recovery to } 1.0 \text{ V})$	t _{fr}	75	ns
Controlled Avalanche Energy (See Test Circuit in Figure 6)	W _{AVAL}	10	mJ

2. Pulse Test: Pulse Width = 300 μ s, Duty Cycle \leq 2.0%.

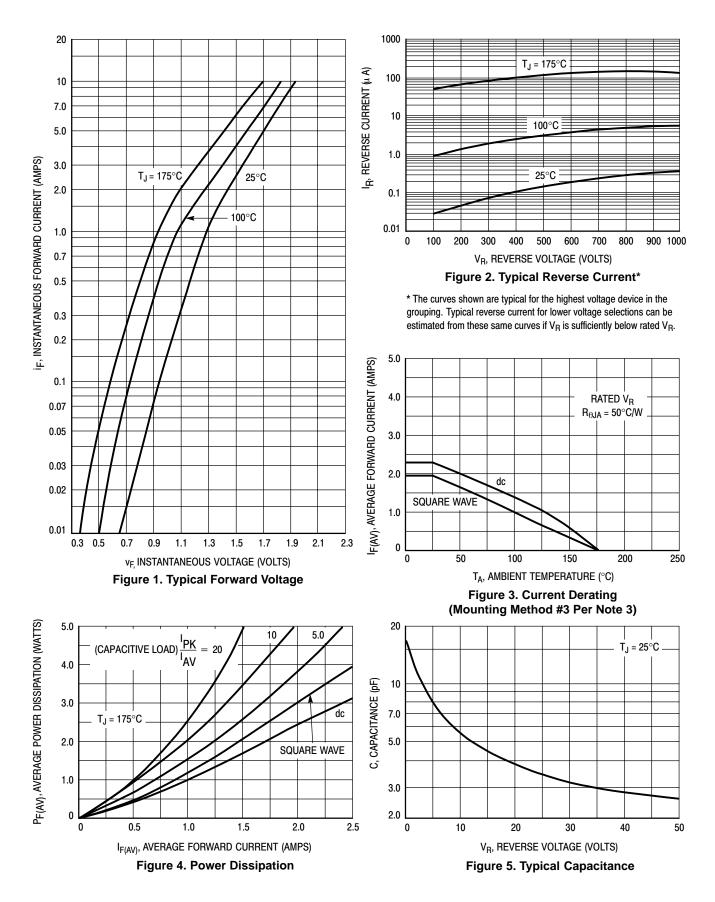
ORDERING INFORMATION

Device	Package	Shipping [†]	
MUR180E	Axial Lead*		
MUR180EG	Axial Lead*	1000 Units / Bag	
MUR180ERL	Axial Lead*	5000 (Toma & David	
MUR180ERLG	Axial Lead*	5000 / Tape & Reel	
MUR1100E	Axial Lead*		
MUR1100EG	Axial Lead*	- 1000 Units / Bag	
MUR1100ERL	Axial Lead*	5000 / Tape & Reel	
MUR1100ERLG	Axial Lead*	JUUU / Tape & Reel	

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

*These packages are inherently Pb-Free.

ELECTRICAL CHARACTERISTICS



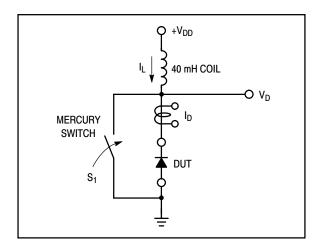


Figure 6. Test Circuit

The unclamped inductive switching circuit shown in Figure 6 was used to demonstrate the controlled avalanche capability of the new "E" series Ultrafast rectifiers. A mercury switch was used instead of an electronic switch to simulate a noisy environment when the switch was being opened.

When S_1 is closed at t_0 the current in the inductor I_L ramps up linearly; and energy is stored in the coil. At t_1 the switch is opened and the voltage across the diode under test begins to rise rapidly, due to di/dt effects, when this induced voltage reaches the breakdown voltage of the diode, it is clamped at BV_{DUT} and the diode begins to conduct the full load current which now starts to decay linearly through the diode, and goes to zero at t_2 .

By solving the loop equation at the point in time when S_1 is opened; and calculating the energy that is transferred to the diode it can be shown that the total energy transferred is equal to the energy stored in the inductor plus a finite amount of energy from the V_{DD} power supply while the diode is in breakdown (from t_1 to t_2) minus any losses due to finite

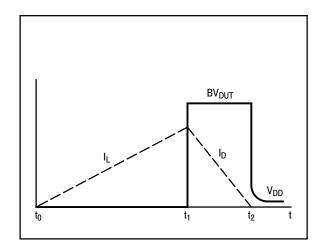


Figure 7. Current–Voltage Waveforms

component resistances. Assuming the component resistive elements are small Equation (1) approximates the total energy transferred to the diode. It can be seen from this equation that if the V_{DD} voltage is low compared to the breakdown voltage of the device, the amount of energy contributed by the supply during breakdown is small and the total energy can be assumed to be nearly equal to the energy stored in the coil during the time when S₁ was closed, Equation (2).

The oscilloscope picture in Figure 8, shows the information obtained for the MUR8100E (similar die construction as the MUR1100E Series) in this test circuit conducting a peak current of one ampere at a breakdown voltage of 1300 V, and using Equation (2) the energy absorbed by the MUR8100E is approximately 20 mjoules.

Although it is not recommended to design for this condition, the new "E" series provides added protection against those unforeseen transient viruses that can produce unexplained random failures in unfriendly environments.



$$W_{AVAL} \approx \frac{1}{2} LI_{LPK}^{2} \left(\frac{BV_{DUT}}{BV_{DUT} - V_{DD}} \right)$$

EQUATION (2):

$$W_{AVAL} \approx \frac{1}{2} LI_{LPK}^2$$

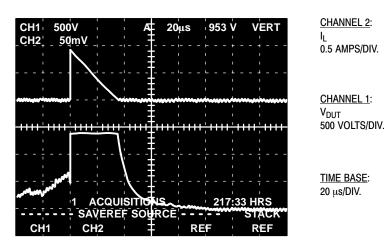


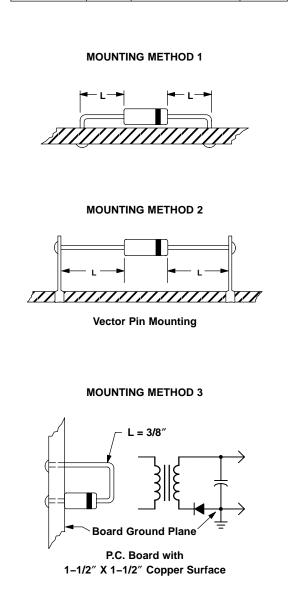
Figure 8. Current–Voltage Waveforms

NOTE 3 — AMBIENT MOUNTING DATA

Data shown for thermal resistance, junction–to–ambient $(R_{\theta JA})$ for the mountings shown is to be used as typical guideline values for preliminary engineering or in case the tie point temperature cannot be measured.

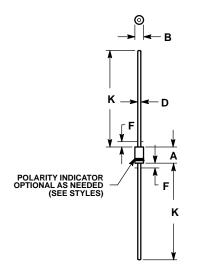
TYPICAL VALUES FOR $\textbf{R}_{\theta \textbf{JA}}$ IN STILL AIR

M	Mounting		Lead Length, L			
1	Method		1/8	1/4	1/2	Units
1			52	65	72	°C/W
2		R _{θJA}	67	80	87	°C/W
3				50		°C/W



PACKAGE DIMENSIONS

AXIAL LEAD CASE 59-10 **ISSUE U**



NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- 2 3.
- ALL RULES AND NOTES ASSOCIATED WITH JEDEC DO-41 OUTLINE SHALL APPLY POLARITY DENOTED BY CATHODE BAND. LEAD DIAMETER NOT CONTROLLED WITHIN F 4.
- 5 DIMENSION.

	INC	HES	MILLIMETERS		
DIM	MIN MAX		MIN	MAX	
Α	0.161	0.205	4.10	5.20	
В	0.079	0.106	2.00	2.70	
D	0.028	0.034	0.71	0.86	
F		0.050		1.27	
κ	1.000		25.40		

STYLE 1: PIN 1. CATHODE (POLARITY BAND)

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